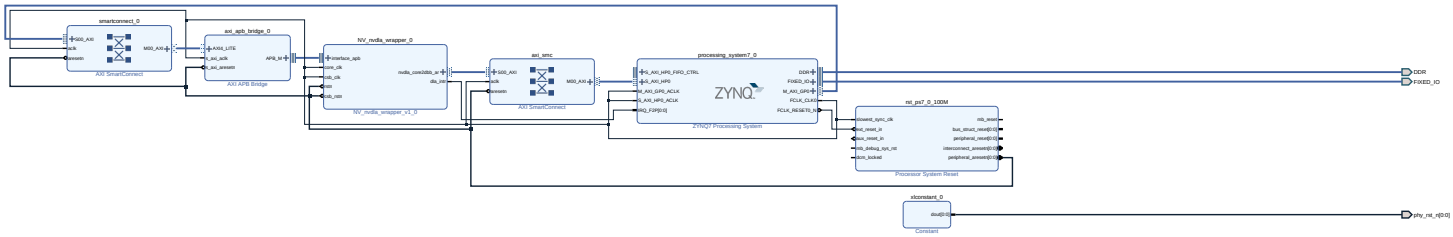
smartconnect_0
S00_AXI
aclk
aresetn
M00_AXI
AXI SmartConnect
axi_apb_bridge_0
AXI4_LITE
s_axi_aclk
s_axi_aresetn
APB_M
AXI APB Bridge
NV_nvdla_wrapper_0
interface_apb
core_clk
csb_clk
rst
csb_rst
nvdla_core2dbb_ar
dbb_rdy
NV_nvdla_wrapper_v1_0
axi_smc
S00_AXI
aclk
aresetn
M00_AXI
AXI SmartConnect
processing_system7_0
S_AXI_HP0_FIFO_CTRL
S_AXI_HP0
M_AXI_GP0_ACLK
S_AXI_HP0_ACLK
IRQ_F2P[0:0]
ZYNQ
DDR
FIXED_IO
M_AXI_GP0
FCLK_CLK0
FCLK_RESET0_N
ZYNQ7 Processing System
rst_ps7_0_100M
slowest_sync_clk
ext_reset_in
aux_reset_in
mb_debug_sys_rst
dcm_locked
mb_reset
bus_struct_reset[0:0]
peripheral_reset[0:0]
interconnect_aresetn[0:0]
peripheral_aresetn[0:0]
Processor System Reset
xlconstant_0
dout[0:0]
Constant
DDR
FIXED_IO
phy_rst_n[0:0]